

<b>Notice of References Cited</b>	Application/Control No. 10/796,109	Applicant(s)/Patent Under Reexamination BROOKS, JOSEPH F.	
	Examiner Asok K. Sarkar	Art Unit 2891	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,314,772	05-1994	Kozicki et al.	430/14
*	B	US-6,086,796	07-2000	Brown et al.	264/1.33
*	C	US-2005/0162883	07-2005	Nejad et al.	365/063
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf and Tauber, "Silicon Processing for the VLSI Era", Volume I, Chapters 12 and 15, Lattice Press, 1986.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.